

SURFACE MOUNT GLASS PASSIVATED SILICON RECTIFIERS

S1A to S1M

**DO214-AA(SMB)
PLASTIC PACKAGE**



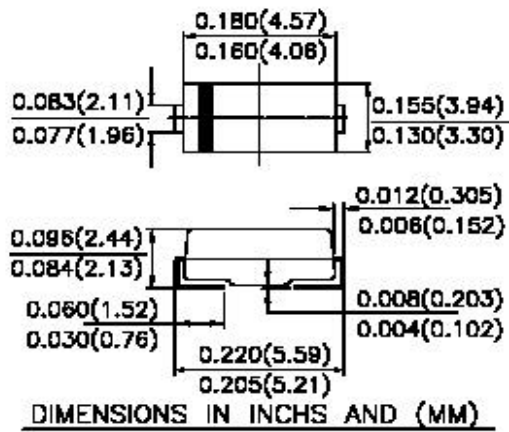
Absolute Maximum Ratings (Ratings at $T_A = 25^\circ\text{C}$ Ambient Temperature unless otherwise specified, Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%)

DESCRIPTION	SYMBOL	S1A	S1B	S1D	S1G	S1J	S1K	S1M	UNIT
Maximum Recurrent Peak Reverse Voltage	V_{RRM}	50	100	200	400	600	800	1000	V
Maximum RMS Voltage	V_{RMS}	35	70	140	280	420	560	700	V
Maximum DC Blocking Voltage	V_{DC}	50	100	200	400	600	800	1000	V
Maximum Average Forward Rectified @ $T_L=100^\circ\text{C}$	$I_{F(AV)}$	1.0							A
Peak Forward Surge Current 8.3ms single half sine wave superimposed on rated load (JEDEC method)	I_{FSM}	30							A
Maximum Forward Voltage @ 1A	V_F	1.1							V
Maximum Reverse Current $T_a=25^\circ\text{C}$	I_R	5.0							mA
@ Rated DC Blocking Voltage $T_a=125^\circ\text{C}$		100							mA
Typical Junction Capacitance (Note 1)	C_J	12							pF
Typical Thermal Resistance (Note 2)	$R_{th(j-l)}$	30							$^\circ\text{C/W}$
Maximum Reverse Recovery Time (Note 3)	t_{rr}	2.5							μS
Operating Junction Temperature Range	T_J	- 55 to +150							$^\circ\text{C}$
Storage Temperature Range	T_{stg}	- 55 to +150							$^\circ\text{C}$

NOTES:

1. Measured at 1MHz and applied reverse voltage of 4.0 VDC
2. Thermal resistance from junction to lead mounted on PCB with 0.3 x 0.3"(8.0 x 8.0mm) copper pad areas
3. Reverse recovery test conditions : $I_F = 0.5\text{A}$, $I_R = 1\text{A}$, $I_{rr} = 0.25\text{A}$

S1A_S1M Rev060704E



Disclaimer

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