

ULTRAFAST RECOVERY RECTIFIERS

UF5400 - UF5408

DO-201AD (Plastic)

Axial Lead Plastic Package



Ultrafast recovery time for high efficiency, Low forward voltage drop, High current capability, Low leakage High surge capability.

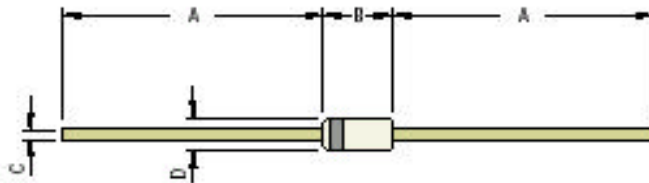
Absolute Maximum Ratings (Ratings at $T_A = 25^\circ\text{C}$ Unless Specified Otherwise, Single Phase, Half Wave Resistive or Inductive Load. For Capacitive Load, derate current by 20%.)

DESCRIPTION	SYMBOL	UF 5400	UF 5401	UF 5402	UF 5403	UF 5404	UF 5405	UF 5406	UF 5407	UF 5408	UNIT
Peak Repetitive Reverse Voltage	V_{RRM}	50	100	200	300	400	500	600	800	1000	V
Maximum RMS Voltage	V_{RMS}	35	70	140	210	280	350	420	560	700	V
Maximum DC Blocking Voltage	V_{DC}	50	100	200	300	400	500	600	800	1000	V
Average Forward Rectified Current 0.375" (9.5mm) Lead Length at $T_A=55^\circ\text{C}$	$I_{F(AV)}$	3.0									A
Peak Forward Surge Current, 8.3ms Single Half Sine-Wave Superimposed on Rated Load (JEDEC method)	I_{FSM}	150.0									A
Maximum Forward Voltage @ 3.0A DC and $T_a=25^\circ\text{C}$	V_F	1.0			1.7					V	
Maximum Reverse Current @ $T_a=25^\circ\text{C}$ at Rated DC Blocking Voltage @ $T_a=100^\circ\text{C}$	I_R	10			1000					μA	
Typical Junction Capacitance (Note 1)	C_J	45			36					pF	
Typical Thermal Resistance (Note 2)	$R_{th(j-a)}$	20									$^\circ\text{C/W}$
Maximum Reverse Recovery Time (Note 3)	t_{rr}	50			75					ns	
Operating and Storage Junction Temperature Range	T_j, T_{stg}	-55 to +150									$^\circ\text{C}$

NOTES:

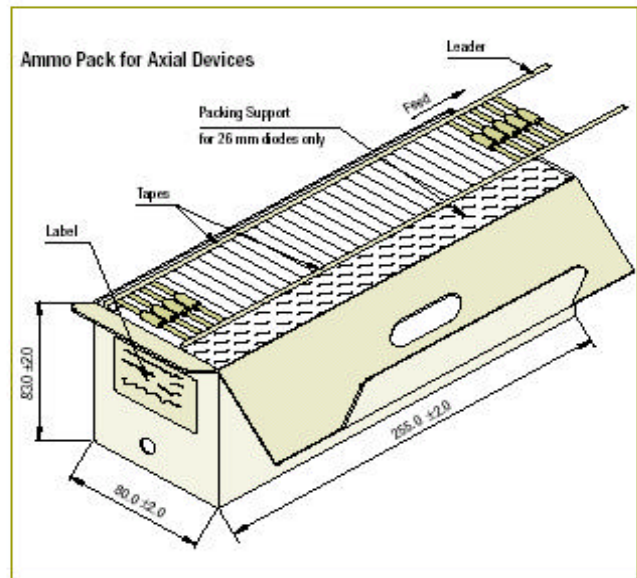
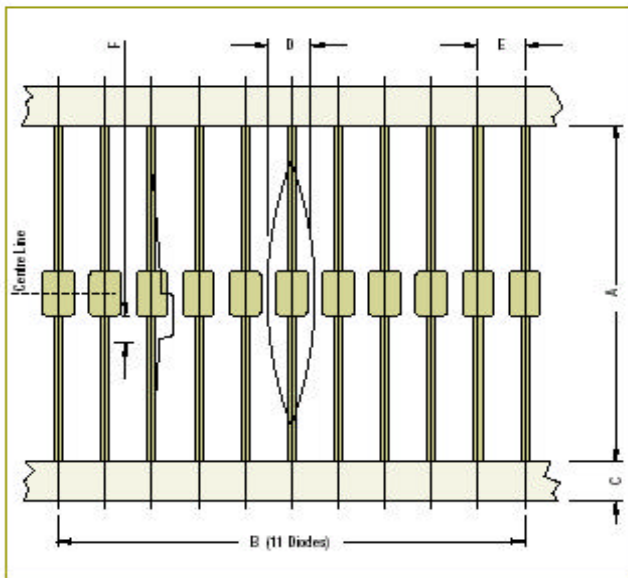
1. Measured at 1MHz and applied reverse voltage of 4.0 VDC.
2. Thermal Resistance Junction to Ambient and from Junction to Lead at 0.375" (9.5mm) lead length P.C.B. mounted with 0.8" x 0.8" (20mm x 20mm) copper pads.
3. Reverse Recovery Test Conditions $I_F=0.5\text{A}$, $I_R=1\text{A}$, $I_{RR}=0.25\text{A}$.

DO-201AD Leaded Plastic Package



DIM	Min	Max
A	25.40	—
B	8.50	9.50
C	1.20	1.30
D	2.00	2.70

DO-201AD Tape and Ammo Packing Specifications



DO-201AD – 52 MM		
DIM	Min	Max
A	50.0	54.0
B	95.0	105.0
C	5.6	6.5
D	—	1.5R
E	9.5	10.5
F	—	1.25

Taping Specification

- 300 mm (Min) leader tape on every roll.
- No. of empty places allowed 0.25% without consecutive empty places.
- Ends of leads shall normally not protrude beyond the tapes.
- Components shall be held sufficiently in the tape or tapes so that they can not come free in normal handling.

Package / Case Type	Packaging Type	Std. Packing		Inner Carton		Outer Carton		
		Qty	Qty	Size L x W x H (cm)	Gross Weight (Kg)	Qty	Size L x W x H (cm)	Gross Weight (Kg)
DO-201AD	T & A	1,200	1.2K	29 x 8 x 15	1.68	10.8K	46 x 36 x 25	15.3

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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